

# PQ30RVI/PQ30RVI 1/PQ30RV2/PQ30RV21

Variable output Low Power-Loss Voltage Regulators

## ■ Features

- Compact resin full-mold package
- Low power-loss (Dropout voltage MAX.0.5V)
- Variable output voltage (setting range :1.5 to 30V)
- Built-in output ON/OFF control function

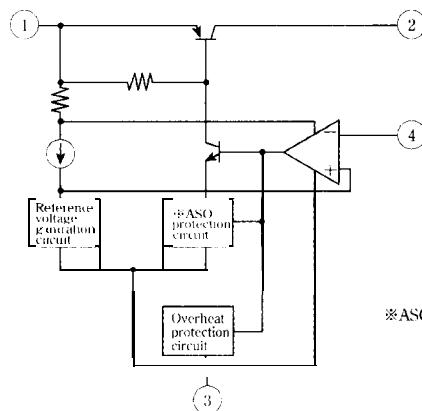
## ■ Applications

- Power supply for print concentration control of electronic typewriters with display
- Series power supply for motor drives
- Series power supply for VCRs and TVs

## ■ Model Line-ups

output voltage	1A output	2A output
Reference voltage precision: $\pm 4\%$	PQ30RVI	PQ30RV2
Reference voltage precision: $\pm 2\%$	PQ30RV11	PQ30RV21

## ■ Equivalent Circuit Diagram

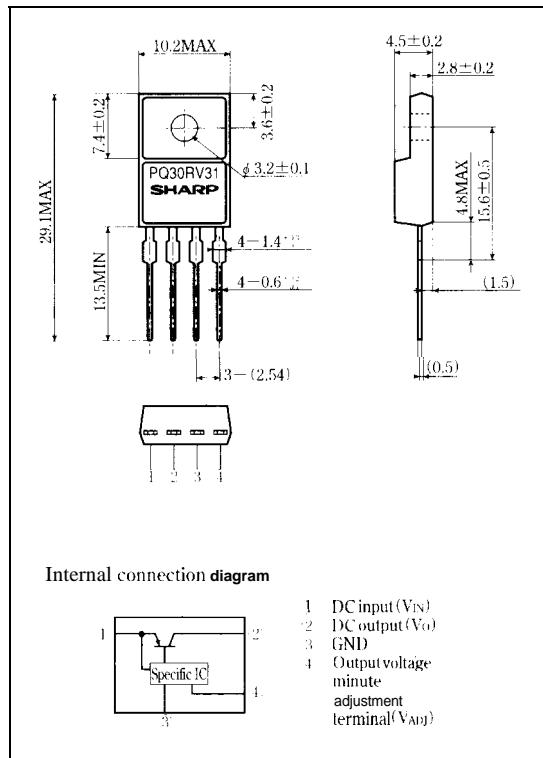


※ASO : Area of Safety Operation

Please refer to the chapter "Handling Precautions".

## ■ Outline Dimensions

(Unit: mm)



## Absolute Maximum Ratings

(Ta=25°C)

Parameter	Symbol	Rating	Unit
*1 Input voltage	VIN	35	v
*1 output voltage adjustment voltage	VADJ	7	v
output current	Io	1	.4
PQ30RVI/PQ30RV11		2	
PQ30RV2/PQ30RV21			
Power dissipation (No heat sink)	PIJ	1.5	W
Power dissipation (With infinite heat sink)	PD2	15	W
		18	
*2 Junction temperature	Tj	150	°C
operating temperature	Tj, Ta	-20~+80	°C
Storage temperature	Tsig	-40~+150	°C
Soldering temperature	Tsol	260 (For 10s)	°C

\*1 All are open except GND and applicable terminals.

\*2 Overheat protection may operate at Tj ≥ 125 °C.

## Electrical Characteristics

Unless otherwise specified, condition shall be

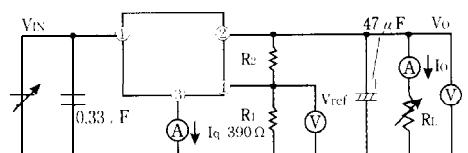
VIN=15V, Vo=10V, Io=0.5A, R1=390Ω (PQ30RVI/PQ30RV11)

VIN=15V, Vn=10V, Io=1.0A, R1=390Ω (PQ30RV2/PQ30RV21)

(Ta=25°C)

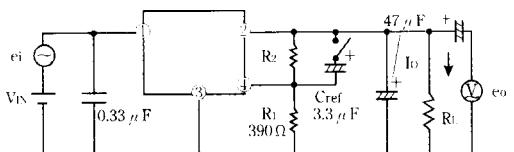
Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Input voltage	VIN		4.5		35	v
output voltage	VO	R2=94Ω to 8.5kΩ	1.5		30	v
		R2=84Ω to 8.7kΩ				
Load regulation	RegL	Io=5mA to 1A		0.3	1.0	%
		Io=5mA to 2A		0.5	1.0	
Line regulation	RegI	VIN=11 to 28V		0.5	2.5	%
Ripple rejection	RR	Cref=0	Refer to Fig. 2	45	55	
		Cref=3.3μF		55	65	dB
Reference voltage	Vref		1.20	1.25	1.30	v
				1.225	1.25	1.275
Temperature coefficient of reference voltage	TeVref	Tj=0 to 125°C		±1.0		%
Dropout voltage	Vd	*3, Io=0.5A			0.5	v
		*3, Io=2A				
Quiescent current	Iq	Io=0			7	mA

\*3 Input voltage shall be the value when output voltage is 95% in comparison with the initial value.

**Fig. 1 Test Circuit**

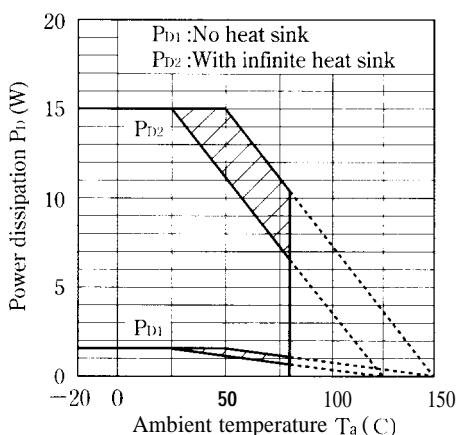
$$V_o = V_{ref} \times \left( 1 + \frac{R_2}{R_1} \right) \approx 1.25 \times \left( 1 + \frac{R_2}{R_1} \right)$$

[R1=390Ω, Vref≈1.25V]

**Fig. 2 Test Circuit of Ripple Rejection**

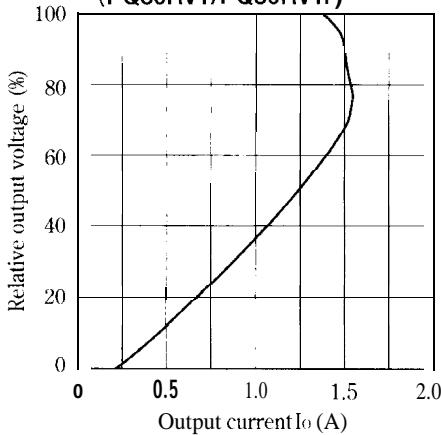
Io=0.5A  
 f=120Hz (sine wave)  
 ei=0.5Vrms  
 RR=20 log (ei/eo)

**Fig. 3 Power Dissipation vs. Ambient Temperature (PQ30RV1/PQ30RV11)**

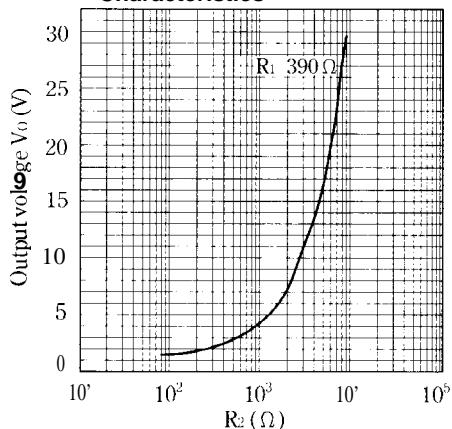


Note) Oblique line portion: Overheat protection may operate in this area,

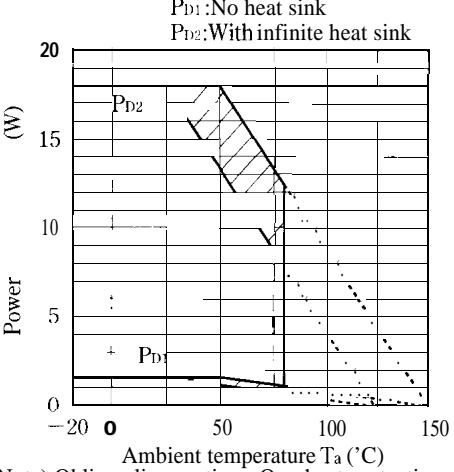
**Fig. 5 Overcurrent Protection Characteristics (PQ30RV1/PQ30RV11)**



**Fig. 7 Output Voltage Adjustment Characteristics**

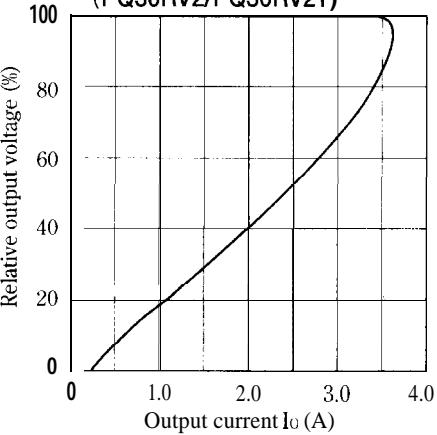


**Fig. 4 Power Dissipation vs. Ambient Temperature (PQ30RV2/PQ30RV21)**

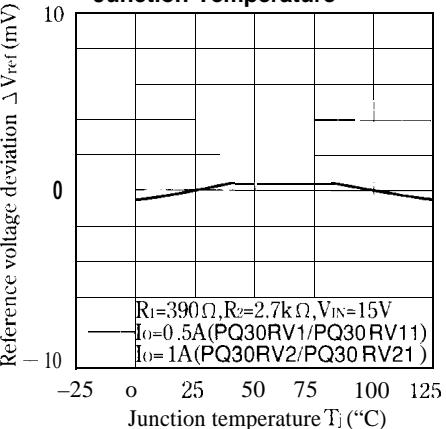


Note) Oblique line portion : Overheat protection may operate in this area.

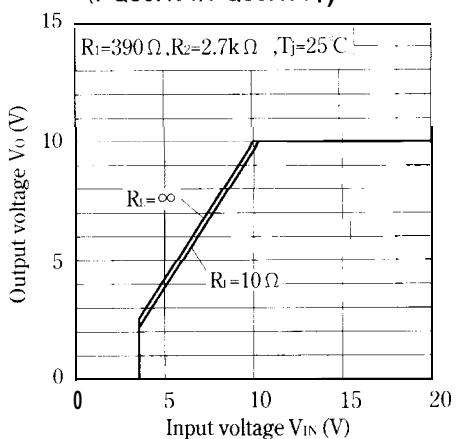
**Fig. 6 Overcurrent Protection Characteristics (PQ30RV2/PQ30RV21)**



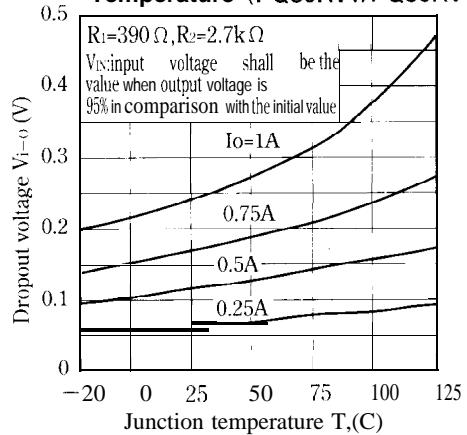
**Fig. 8 Reference Voltage Deviation vs. Junction Temperature**



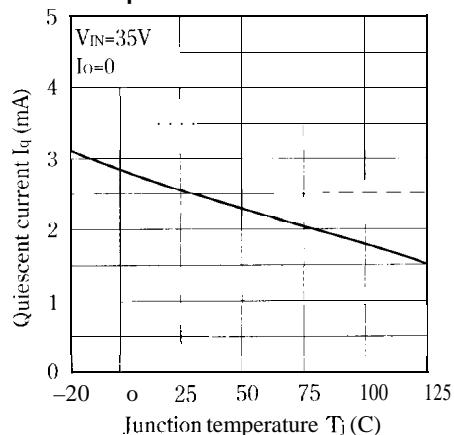
**Fig. 9 Output Voltage vs. Input Voltage (PQ30RV1/PQ30RV11)**



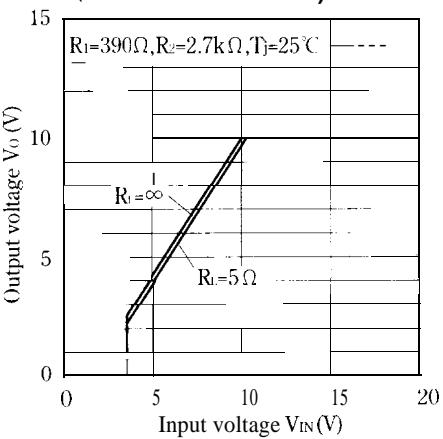
**Fig.11 Dropout Voltage vs. Junction Temperature (PQ30RV1/PQ30RV11)**



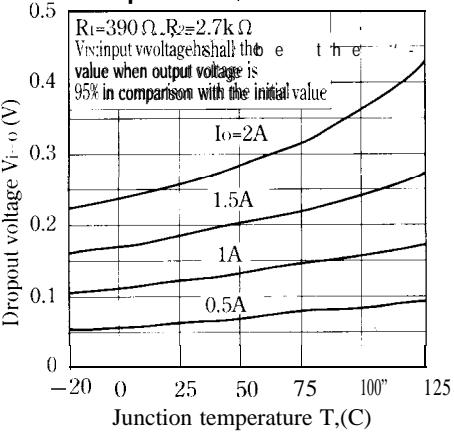
**Fig.13 Quiescent Current vs. Junction Temperature**



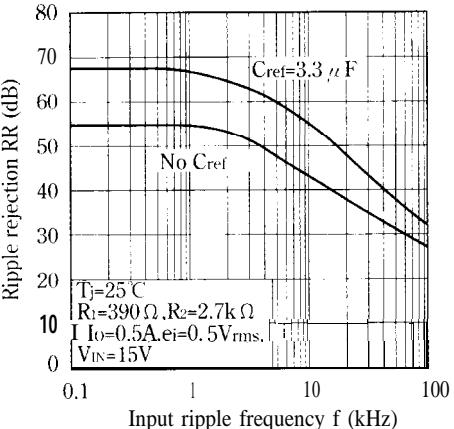
**Fig.10 Output Voltage vs. Input Voltage (PQ30RV2/PQ30RV21)**

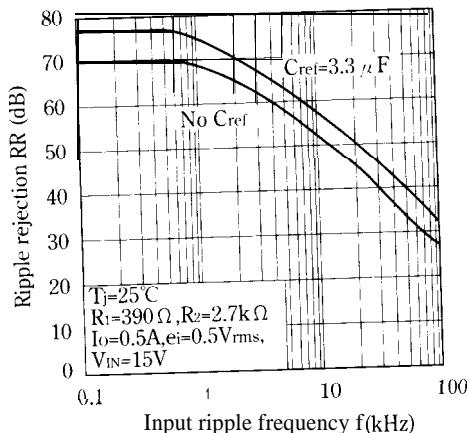
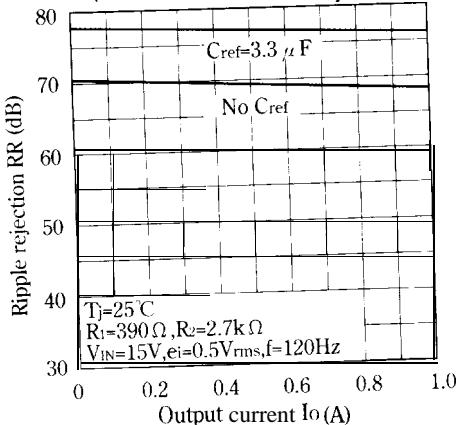
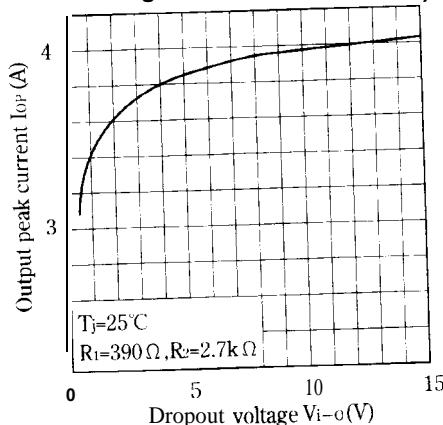
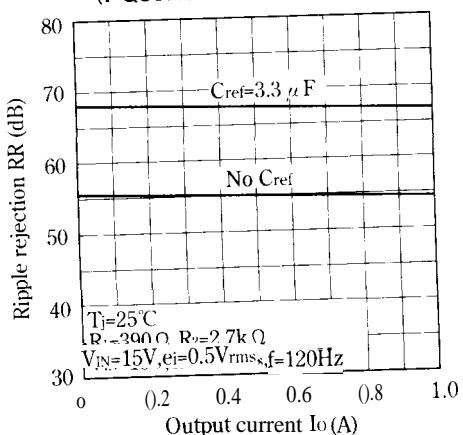
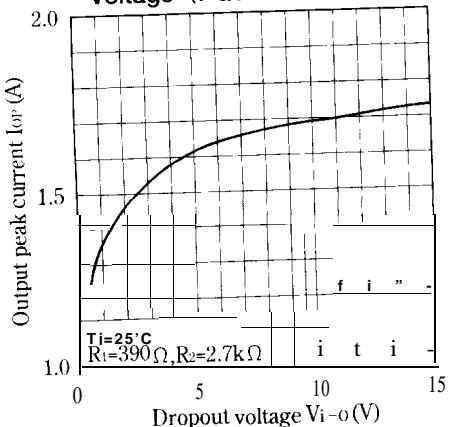
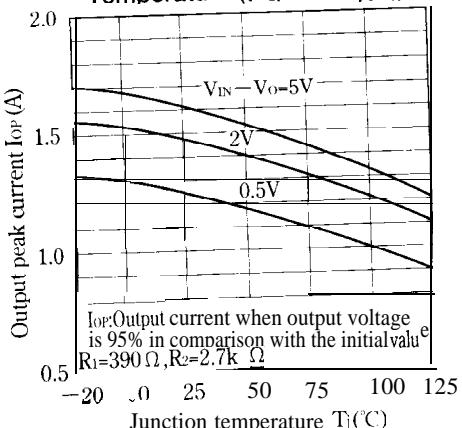


**Fig.12 Dropout Voltage vs. Junction Temperature (PQ30RV2/PQ30RV21)**

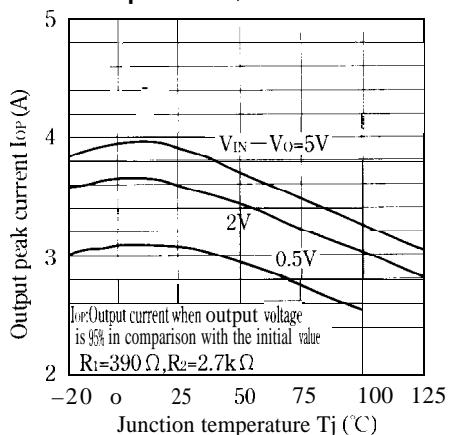


**Fig.14 Ripple Rejection vs. Input Ripple Frequency (PQ30RV1/PQ30RV11)**

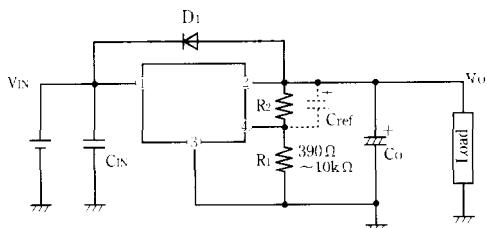


**Fig.15 Ripple Rejection vs. Input Ripple Frequency (PQ30RV2/PQ30RV21)****Fig.17 Ripple Rejection vs. Output Current (PQ30RV2/PQ30RV21)****Fig.19 Output Peak Current vs. Dropout Voltage (PQ30RV2/PQ30RV21)****Fig.16 Ripple Rejection vs. Output Current (PQ30RVI/PQ30RVI 1)****Fig.18 Output Peak Current vs. Dropout Voltage (PQ30RVI/PQ30RVI 1)****Fig.20 Output Peak Current vs. Junction Temperature (PQ30RVI/PQ30RVI 1)**

**Fig.21 Output Peak Current vs. Junction Temperature (PQ30RV2/PQ30RV21)**



## ■ Standard Connection



1) This device is necessary to protect the element from damage when reverse voltage maybe applied to the regulator in case of input short-circuiting.

C<sub>ref</sub> : This device is necessary when it is required to enhance the ripple rejection or to delay the output start-up time(\* 1). (\* 1)Otherwise, it is not necessary.

(Care must be taken since C<sub>ref</sub> may raise the gain, facilitating oscillation.)

(\* 1) The output start-up time is proportional to C<sub>ref</sub> X R<sub>2</sub>.

C<sub>IN</sub>, C<sub>O</sub> : Be sure to mount the devices C<sub>IN</sub> and C<sub>O</sub> as close to the device terminal as possible so as to prevent oscillation.

The standard specification of C<sub>IN</sub> and C<sub>O</sub> is 0.33 μF and 47 μF, respectively. However, adjust them as necessary after checking.

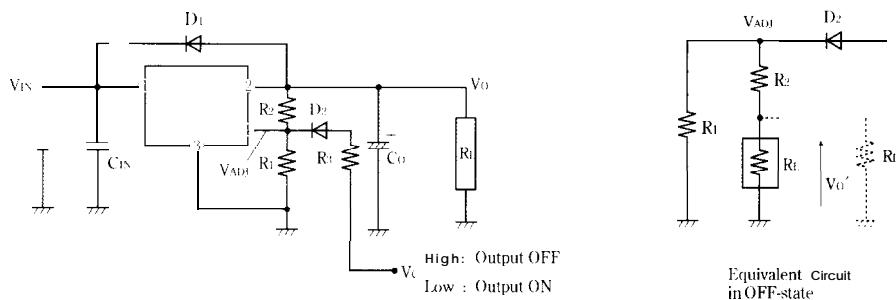
R<sub>1</sub>, R<sub>2</sub> : These devices are necessary to set the output voltage. The output voltage V<sub>O</sub> is given by the following formula:

$$V_O = V_{ref} \times (1 + R_2/R_1)$$

(V<sub>ref</sub> is 1.25V TYP)

The standard value of R<sub>1</sub> is 390 Ω. But value up 10k Ω does not cause any trouble.

## ■ ON/OFF Operation



- ON/OFF operation is available by mounting externally D<sub>2</sub> and R<sub>3</sub>.
- When V<sub>ADJ</sub> is forcibly raised above V<sub>ref</sub> (1.25V TYP) by applying the external signal, the output is turned off (pass transistor is turned off). When the output is OFF, V<sub>ADJ</sub> must be higher than V<sub>ref</sub> MAX., and at the same time must be lower than maximum rating 7V.

In OFF-state, the load current flows to R<sub>L</sub> from V<sub>ADJ</sub> through R<sub>2</sub>. Therefore the value of R<sub>2</sub> must be as high as possible.

- $V_{O'} = V_{KII} \times R_L / (R_L + R_2)$

occurs at the load. OFF-state equivalent circuit R<sub>L</sub> up to 10kΩ is allowed. Select as high value of R<sub>L</sub> and R<sub>2</sub> as possible in this range. In some case, as output voltage is getting lower (V<sub>O</sub> < 1V), impedance of load resistance rises. In such condition, it is sometime impossible to obtain the minimum value of V<sub>O'</sub>. So add the dummy resistance indicated by R<sub>D</sub> in the figure to the circuit parallel to the load,

## ■ An Example of ON/OFF Circuit Using the 1 -chip Microcomputer Output Port (PQ30RVI )

( Specification)  
output port of microcomputer  
 $V_{OH}(\text{max}) = 0.5 \text{ V}$   
 $V_{OH}(\text{min}) = 2.41 \text{ (} I_{OH} = 0.2\text{mA} \text{)}$   
MAX, rating of  $I_{OH} = 0.5\text{mA}$   
Output should be set as follows  
 $15.6V R_L = 52 \Omega \text{ (} I_L = 0.3\text{A} \text{)}$

From  $V_{O'} = V_{KII} (1 + R_2/R_L)$  we get  $V_{O'} = 15.6\text{V}$ .

$$R_2/R_L = 11.48$$

Assuming that  $V_F(\text{max}) = 0.8\text{V}$  for D<sub>2</sub> in case of  $V_{OH}(\text{min}) = 2.4\text{V}$ , we get  $V_{ADJ} = V_{OH}(\text{min}) - V_F(\text{max}) = 2.4\text{V} - 0.8\text{V} = 1.6\text{V}$ . From  $V_{ref}(\text{max}) = 1.3\text{V}$  we get  $R_3 = 0 \Omega$

If  $R_L = 10\text{k}\Omega$ , we get  $R_2 = 11.48 \times R_L = 114.8\text{k}\Omega$  and  $I_{OH}$  as follows, ignoring R<sub>L</sub> (52 Ω):

$$I_{OH} = 1.6\text{V} \times (R_L + R_2) / R_L \times R_2$$

$$= 1.6\text{V} \times (10\text{k}\Omega + 114.8\text{k}\Omega) / 10\text{k}\Omega \times 114.8\text{k}\Omega = 0.17\text{mA}$$

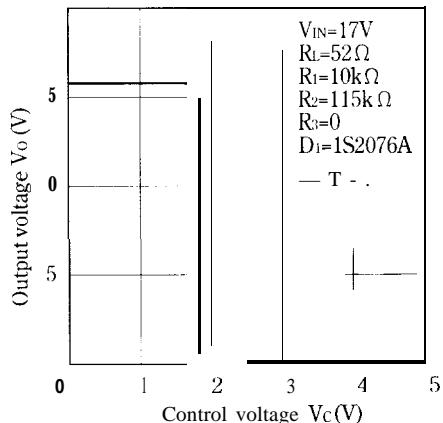
Hence,  $I_{OH} < 0.2\text{mA}$ . Therefore  $V_{OH}(\text{min})$  is ensured.

Next, assuming that  $V_F(\text{min}) = 0.5\text{V}$  for D<sub>2</sub> in case of  $V_{OH}(\text{max})$ , we get :

$$I_{OH} = (5\text{V} - 0.5\text{V})(R_L + R_2) / R_L \times R_2 = 0.49\text{mA}$$
 which is less than the rating.

Figure 1 shows the  $V_O - V_C$  characteristics when  $R_L = 10\text{k}\Omega$ ,  $R_2 = 115\text{k}\Omega$ ,  $R_3 = 0\Omega$ ,  $V_{IN} = 17\text{V}$ ,  $R_L = 52\Omega$ , and D<sub>1</sub> = 1S2076A (Hitachi).

## Output Voltage vs. Control Voltage (PQ30RVI)

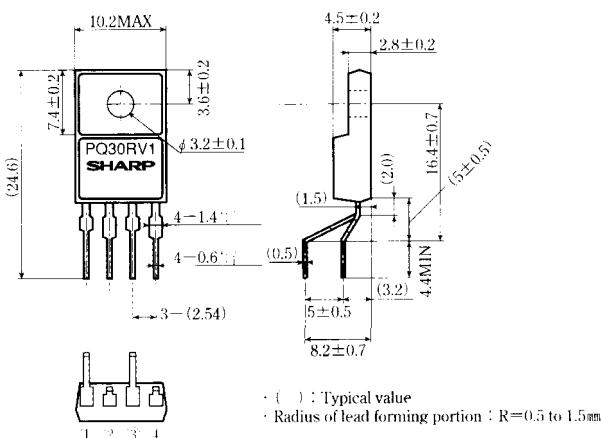


## ■ Model Line-ups for Lead Forming Type

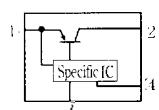
Output voltage	5V output	2A output
Output voltage precision: $\pm 2.5\%$	PQ30RVI B	PQ30RV2B

## ■ Outline Dimensions (PQ30RV1B/PQ30RV2B)

(Unit: mm)



Internal connection diagram



- 1 DC input ( $V_{IN}$ )
- 2 DC output ( $V_O$ )
- 3 GND
- 4 Output voltage minute adjustment terminal ( $V_{AB1}$ )

Note) The value of absolute maximum ratings and electrical characteristics is same as ones of PQ30RVI /2 series